

SVARM 2010

Synthesis, Verification, and Analysis of Rich Models - COST Action IC0901 WG+MC Meeting - PSY 2010: Practical Synthesis for Concurrent Systems Joint EMS+QMS Panel

Tuesday, July 20: Synthesis and Infrastructure

08:50	Paul Jackson	Welcome and Opening
<i>Session Chair: Roderick Bloem</i>		
09:00	Ras Bodik	Next Steps in Partial-Program Synthesis
09:30	Kim Larsen	Controller Synthesis from Timed Game Automata – from Theory to Practice
10:00	Coffee Break	
<i>Session Chair: Cesar Sanchez</i>		
10:30	Alexander Rabinovich	Extensions of the Church synthesis Problem
11:00	Bernd Finkbeiner	Coordination Logic
11:30	Georg Hofferek and Roderick Bloem	Controller Synthesis Using Uninterpreted Functions
12:00	Tobias Nipkow	Automatic proof and disproof in Isabelle
12:30	LUNCH	
<i>Session Chair: Paul Jackson</i>		
14:00	Natarajan Shankar	Inference Architectures for Satisfiability Modulo Theories
15:00	Coffee Break	
15:30		<u>Panel joint with Evaluation Methods for Solvers and Quality Metrics for Solutions</u>
16:15 -		Discussion after the panel
<i>Session Chair: Enric Rodríguez Carbonell</i>		
17:00	Armin Biere and Florian Lonsing	Extending the BTOR Language
17:30	Management Committee	<u>Management Committee Meeting of Action IC0901</u>
20:00		<u>Organized Dinner in David Bann vegetarian restaurant</u>

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Wednesday, July 21

Verification and Automated Reasoning

<i>Session Chair: Tobias Nipkow</i>		
09:00	Bernhard Beckert	Formal Verification of System Software
10:00	Short Coffee Break	
<i>Session Chair: Tayssir Touili</i>		
10:20	Peter Schneider-Kamp	Towards Complexity and Termination Analysis of Transition Systems
10:40	Peter Habermehl, Lukas Holik, Adam Rogalewicz, Jiri Simacek and Tomas Vojnar	A Proposal of a New Automata-based Representation of Heaps
11:00	Marius Bozga, Radu Iosif, Filip Konecny and Tomas Vojnar	FLATA: Towards a Toolset for manipulation and analysis of counter automata
11:20	Mini Break of 10 minutes	
<i>Session Chair: Predrag Janičić</i>		
11:30	Stefan Ratschan	Verification of Mixed Discrete-Continuous Systems
11:50	Alejandro Sanchez and Cesar Sanchez	Towards Temporal Verification of Concurrent Data-structures: In Need for Sophisticated Decision Procedures
12:10	Hossein Hojjat, Viktor Kuncak, Ruzica Piskac and Philippe Suter	Vepar: A Framework for Automated Reasoning
12:30	LUNCH	
<i>Session Chair: Maria Paola Bonacina</i>		
14:00	Paul Jackson and Grant Passmore	Applications of a procedure for solving non-linear arithmetic problems
14:20	Cristina Borralleras, Salvador Lucas, Albert Oliveras, Enric Rodríguez Carbonell and Albert Rubio	Solving Non-linear Polynomial Arithmetic via SAT Modulo Linear Arithmetic
14:40	Timothy Nelson, Dougherty Daniel, Kathi Fisler and Shriram Krishnamurthi	On the Finite Model Property in Order-Sorted Logic
15:00	Short Coffee Break	
<i>Session Chair: Viktor Kuncak</i>		
15:20	Mariangiola Dezanì-Ciancaglini, Silvia Ghilezan, Svetlana Jaksic and Jovanka Pantovic	Types for dynamic web data with RBAC
15:40	Predrag Janicic and Filip Maric	Uniform reduction to SMT
16:00	Maria Paola Bonacina and Moa Johansson	Towards an Interpolating First-Order Prover
16:20	Closing Discussions	

