



Model Checking

BMC, SAT based, BDDs

Hardware Model Checking Competition

SAT

preprocessing, restarts

PicoSAT, Precosat

SMT (Satisfiability Modulo Theory)

Boolector: bit-vectors, arrays

debugging, testing

QBF (Quantified Boolean Formulas)

resolve & expand (Quantor)

dependencies, NNF



Armin Biere



Robert Brummayer



Florian Lonsing

