

Controller Synthesis Using Uninterpreted Functions

Overview of a work in progress

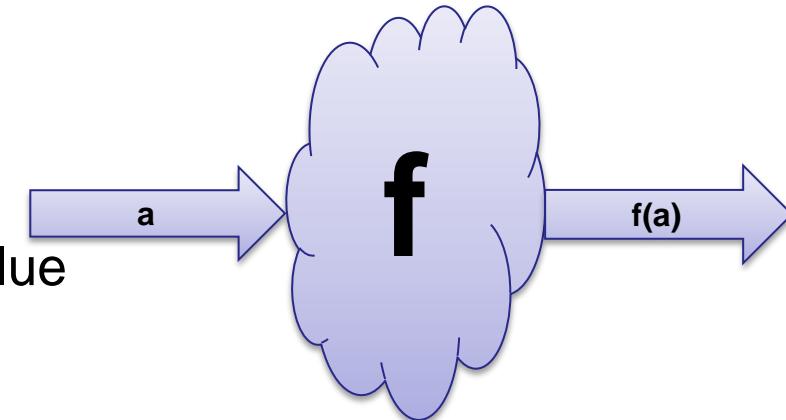
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What is an Uninterpreted Function?

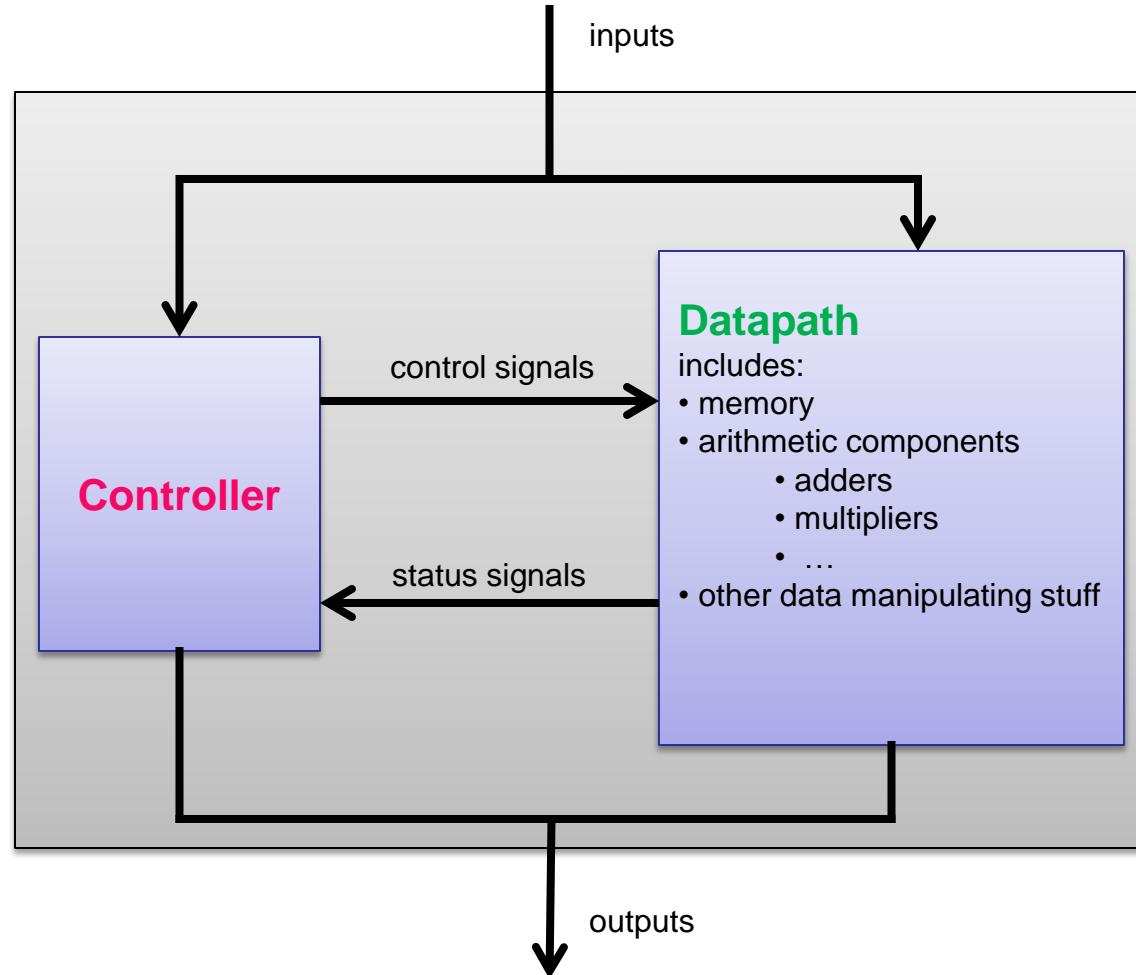
- A **function**... (obviously) ☺
 - Possibly n-ary
 - Mapping input value(s) to output value
- ... which is **uninterpreted**.
 - i.e., we do not know/care about its “internals”
- But: **functional consistency**
 - $(a = b) \implies f(a) = f(b)$



for n-ary function:

$$\left(\bigwedge_{i \in \{1, \dots, n\}} a_i = b_i \right) \implies f(a_1, a_2, \dots, a_n) = f(b_1, b_2, \dots, b_n)$$

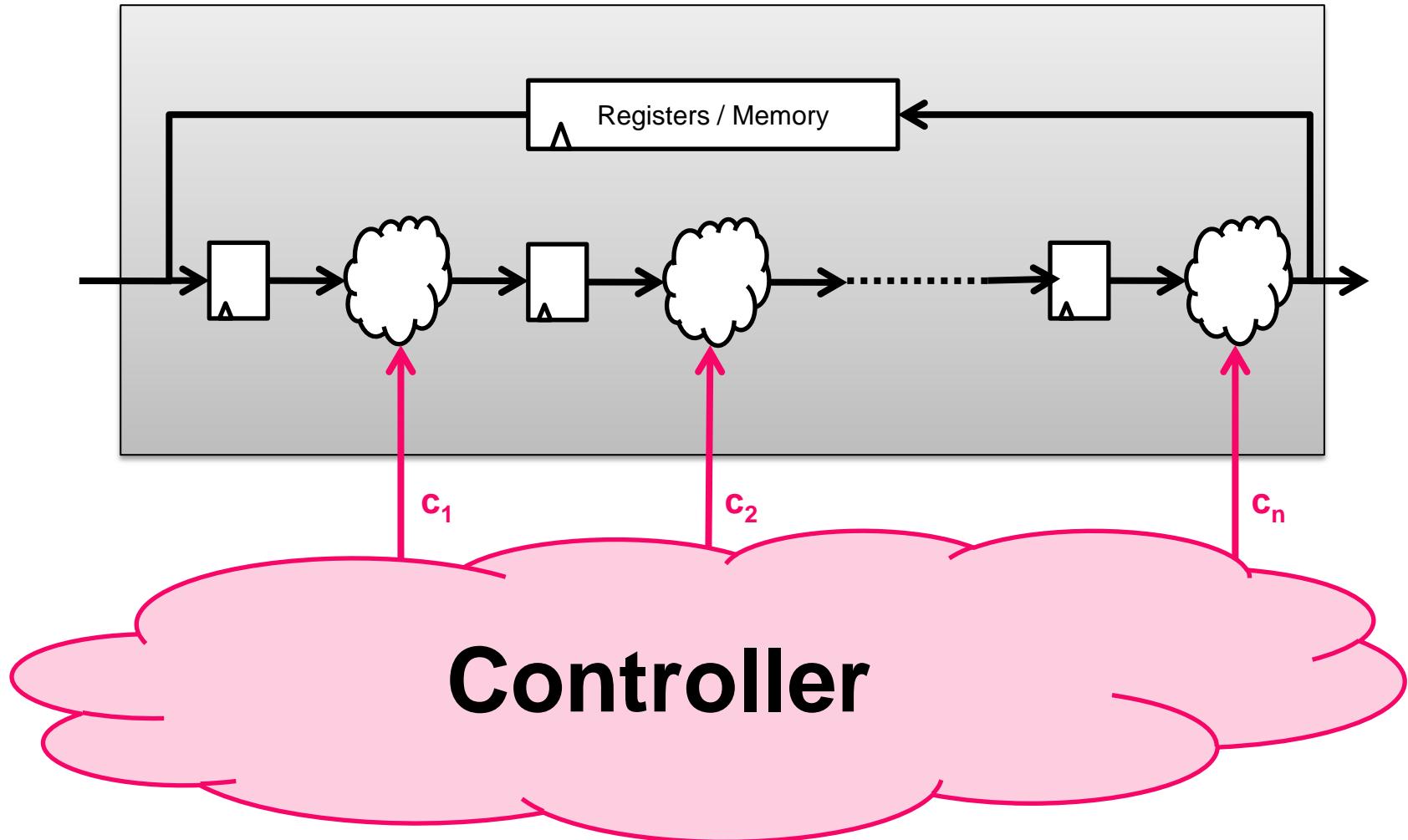
What is a controller?



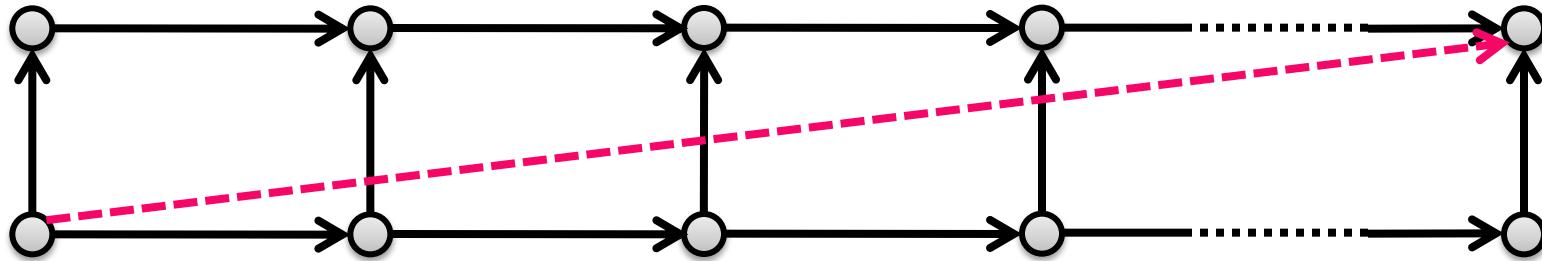
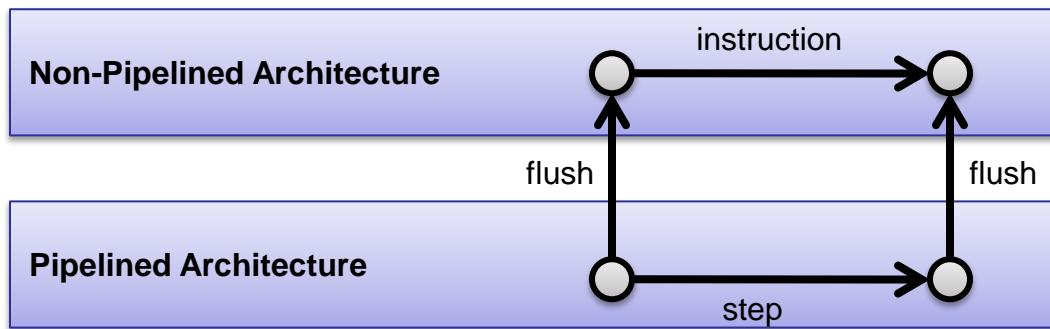
Controller versus **Datapath**
are like:

- **Driver** versus **Car**
- **Musician** versus **Piano**
- ...

Motivation: Pipelined Microprocessor

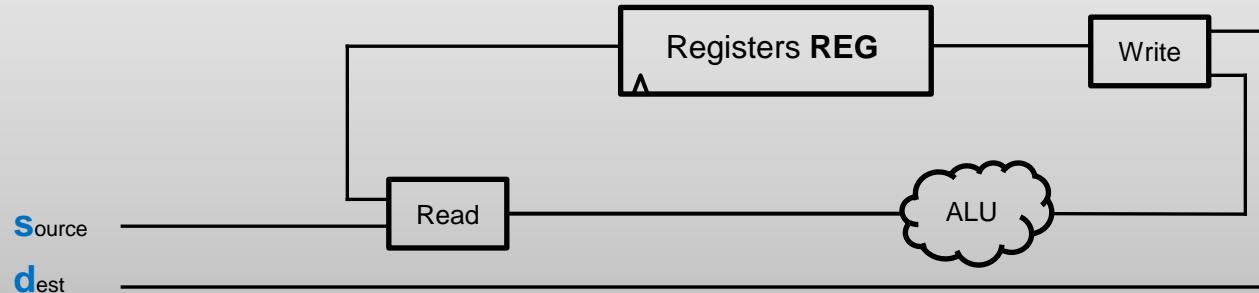


Equivalence: Commutativity

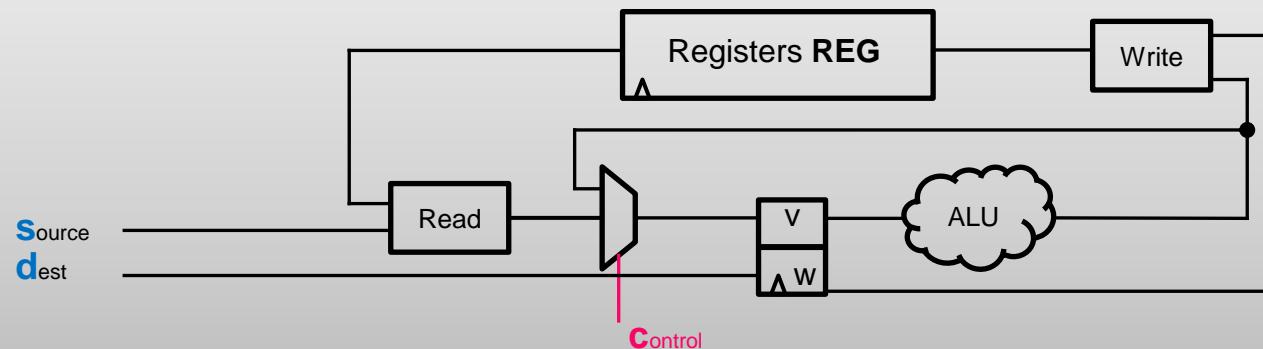


(Very) Simple Example

Non-pipelined Architecture
(=reference):



Pipelined Architecture:



Synthesis Approach

- Define equivalence criterion: φ
- Claim: $\forall \bar{R} . \forall \bar{f} . \forall \bar{i}, \bar{s} . \exists \bar{c} . \exists \bar{R}' . \exists \bar{s}' . \varphi$
- **Reads:** “*For all (initial) array contents, for all interpretations of the functions, and for all inputs and initial states, there are control values, and resulting new array contents and next states, such that the equivalence criterion evaluates to true.*”
- If the claim is valid, extract $\bar{c} \doteq \bar{c}(\bar{f}, \bar{i}, \bar{s})$

Example: Equivalence Criterion

complete – ISA: 

$$\varphi_{cI} = [REG'_{cI} \Leftarrow REG\{w \leftarrow ALU(v)\}] \wedge [REG''_{cI} \Leftarrow REG'_{cI}\{d \leftarrow ALU(REG'_{cI}[s])\}] \quad \begin{cases} \text{complete} \\ \text{ISA} \end{cases}$$

step – complete: 

$$\varphi_{sc} = [REG'_{sc} \Leftarrow REG\{w \leftarrow ALU(v)\}] \wedge (w' = d) \wedge [v' = (c ? ALU(v) : REG[s])] \wedge [REG''_{sc} \Leftarrow REG'_{sc}\{w' \leftarrow v'\}] \quad \begin{cases} \text{step} \\ \text{complete} \end{cases}$$

Equivalence criterion:

$$\varphi \Leftrightarrow \varphi_{cI} \wedge \varphi_{sc} \wedge \forall i . (REG''_{cI}[i] = REG''_{sc}[i])$$

Transformations

- Equivalence criterion is a first-order formula, using the theories of
 - Arrays (A)
 - Uninterpreted Functions (U)
 - Equality (E)
- Three reductions/transformations:
 - $A \cup E \rightarrow U \cup E$  (proof done)
 - $U \cup E \rightarrow E$  (proof done)
 - $E \rightarrow \text{Propositional Logic}$  (proof in progress)

A-U-E → U-E

1. Replace Array-Writes with fresh variables and apply **write axiom**
 $A\{i \leftarrow v\} \rightsquigarrow A' : (\forall j \neq i . A'[j] = A[j]) \wedge A'[i] = v$
 2. Replace existential quantifications with fresh variables
 3. Replace universal quantifications with conjunction over index set
 4. Replace Array-Reads with uninterpreted functions $A[i] \rightsquigarrow A(i)$
-

$$\forall \bar{R} . \forall \bar{f} . \forall \bar{i}, \bar{s} . \exists \bar{c} . \exists \bar{R}' . \exists \bar{s}' . \varphi_{A,U,E}$$



$$\forall \bar{f}, \bar{f}_{R'} . \forall \bar{i}, \bar{s} . \exists \bar{c} . \exists \bar{f}_{R'} . \exists \bar{s}', \lambda . \varphi_{U,E}$$

Ackermann's Reduction: UIF-E \rightarrow E

- Replace all function instances with fresh variables $f(x) \rightsquigarrow \hat{f}_x$ and thus obtain $\overline{\varphi_{U,E}}$
- Add functional consistency constraints $FC \Leftrightarrow [(x = y) \rightarrow (\hat{f}_x = \hat{f}_y)] \wedge \dots$ and obtain

$$\varphi_e \Leftrightarrow FC_{f,f_R} \rightarrow (FC_{f_{R'}} \wedge \overline{\varphi_{U,E}})$$

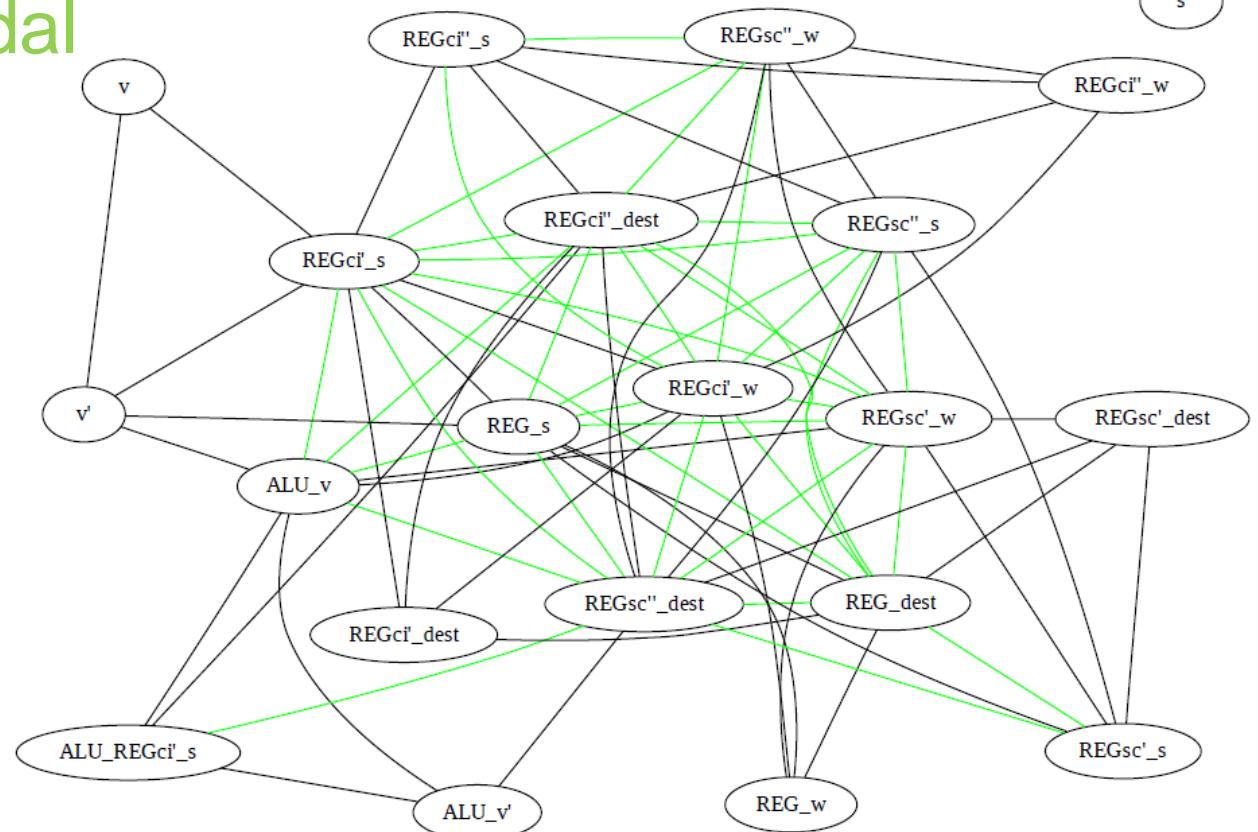
$$\forall \bar{f}, \bar{f}_R . \forall \bar{i}, \bar{s} . \exists \bar{c} . \exists \bar{f}_{R'} . \exists \bar{s}', \lambda . \varphi_{U,E}$$



$$\forall \widehat{f^u}, \bar{f}_R^u, \bar{i}, \bar{s} . \exists \bar{c} . \exists \widehat{f_{R'}^{u,e}}, \bar{s}', \lambda . \forall \widehat{f^e}, \widehat{f}_R^e . \varphi_e$$

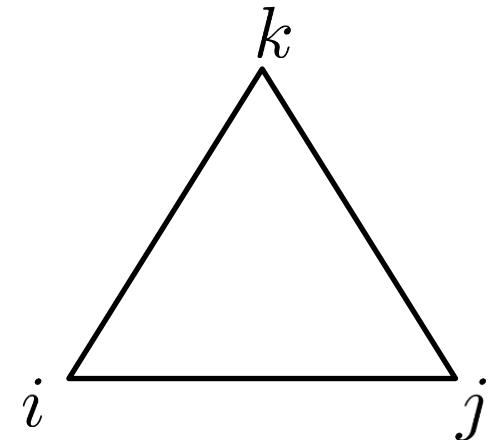
$E \rightarrow$ Prop. Logic (Graph-based)

- Build the non-polar equality graph
 - Make it chordal



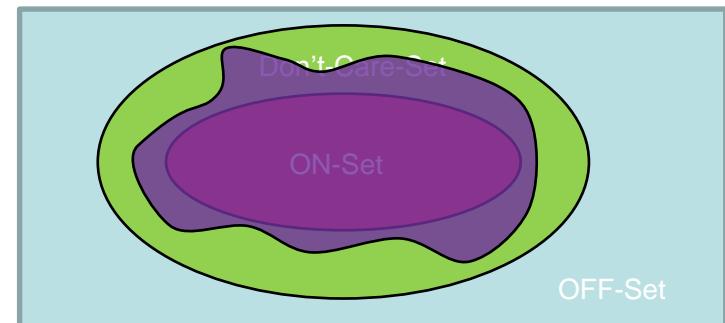
$E \rightarrow \text{Prop. Logic}$ (continued)

- Replace equalities with fresh Boolean variables
 $(x = y) \rightsquigarrow e_{x,y}$
- For each triangle (i, j, k) in the equality graph, add the following conjunct to \mathcal{B}_{trans}
$$(e_{i,j} \wedge e_{j,k} \rightarrow e_{i,k}) \wedge$$
$$(e_{i,j} \wedge e_{i,k} \rightarrow e_{j,k}) \wedge$$
$$(e_{i,k} \wedge e_{j,k} \rightarrow e_{i,j})$$
- Open point:
 - Respect quantifier structure



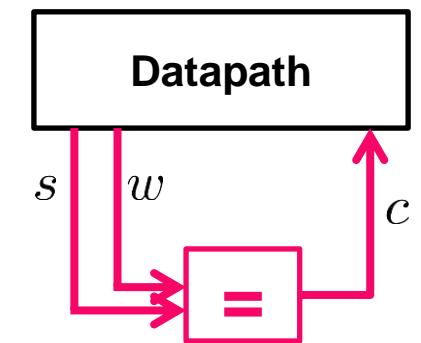
Extract Function for Control Logic

- We started from: $\forall \bar{R} . \forall \bar{f} . \forall \bar{i}, \bar{s} . \exists \bar{c} . \exists \bar{R}' . \exists \bar{s}' . \varphi$
- Apply transformations, obtain φ_{prop}
- Existentially quantify “next states” \bar{s}', \bar{R}'
 - i.e., quantify all variables which “come from” one of the next state variables. E.g. $e_{REG''_s, REG'_s}$
- Expand existential quantification of \bar{c}
 - Example: $\exists c . \varphi_{prop} \Leftrightarrow (c \wedge \varphi_{prop}) \vee (\neg c \wedge \varphi_{prop})$
- Find cofactors of c
 - Positive Cofactor: ON-Set + DC-Set
 - Negative Cofactor: OFF-Set + DC-Set
- Find function in this interval



Results / Summary

- We started from
 - a **datapath** of the target system
 - a **reference implementation**
 - an **equivalence criterion**
- We obtained
 - Boolean function(s) for the control logic
 - in terms of
 - (dis-)equalities between inputs and states
 - Example: $c \Leftrightarrow (s = w) \Leftrightarrow e_{s,w}$



Open Points / Current Problems

- Proof(s) for Transformations
 - unfinished
- Practical issues
 - Runtime complexity?
 - Efficiency:
 - BDDs
 - SMT Solvers
- “Implementation”
 - Only hardcoded for simple pipeline example
 - Based on BDD operations
 - Not even (completely) finished

